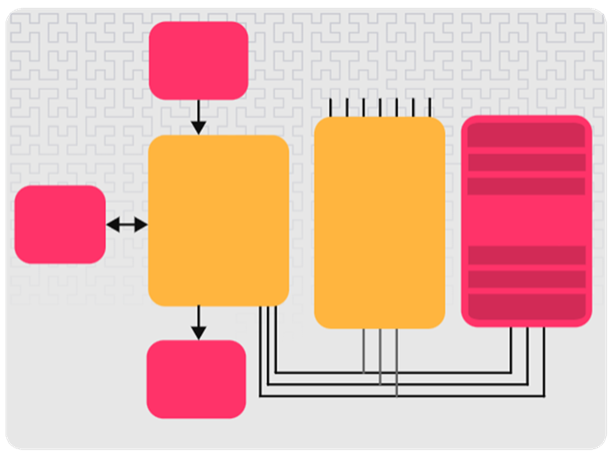
**System Architecture**

**Handout 1 – Von Neumann Architecture**

**Task 1**

Using the diagram below, complete the table to identify the different sections of the **von Neumann architecture**.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Input devices | Processor | Main memory | Secondary storage | Input/ Output controllers |



|  |  |
| --- | --- |
| **1** |  |
| **2** |  |
| **3** |  |
| **4** |  |
| **5** |  |
| **6** |  |

**Fill in the gaps.**

|  |  |  |
| --- | --- | --- |
| main memory | program instructions | data |

The Von Neumann architecture used the idea of storing \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ and \_\_\_\_\_\_\_\_\_\_\_\_  in \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ and moving them between memory and the processor.

**Registers**

|  |  |
| --- | --- |
| **Acronym** | **Meaning** |
| MAR |  |
| MDR |  |
| ACC |  |
| PC |  |

**Task 2**

Identify the correct description to match the **bus.**

|  |  |  |
| --- | --- | --- |
|  |  | transfers content to/from locations |
|  |  | synchronise and control operations |
|  |  | identifies address location |